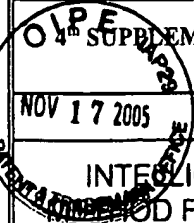| U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/724,588
O 4ᵗʰ SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: November 28, 2003
	Inventors: Boucher, et al.
NOV 1 7 2005	Group Art Unit: 2154
INTELLIGENT NETWORK INTERFACE SYSTEM AND METHOD FOR ACCELERATED PROTOCOL PROCESSING	Examiner name: Wen Tai Lin
	Attorney Docket No.: ALA-025

U.S. Patent Documents

*Examiner Initial	Cite No.	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
Cuf	1	6,067,569	5/23/00	Khaki et al.	709	224	

Foreign Patent Documents

*Examiner Initial	Cite No.	Document Number	Date	Country	Class	Subclass	Yes	No
Cuf	2	WO 01/59966	8/16/01	WO ~~PCT/US00/06475~~				
Cuf	3	WO 01/86430	11/15/01	WO ~~PCT/US01/15180~~				

Other Art—Non Patent Literature Documents

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
Cuf	4	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
A	5	Rice University article entitled "LRP: A Network Subsystem Architecture for Server Systems", by P. Druschel and G. Banga, 14 pages. Oct, 1996
	6	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/ftp/rfc/pdf/rfc2140.txt.pdf, 11 pages, April 1997.
	7	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
	8	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
	9	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
	10	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
	11	Thia, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", Protocols for High Speed Networks, pages 53-68, 1993.
	12	Thia, Y.H. Publication entitled " A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", Protocols for High Speed Networks, pages 224-239, 1995.
Cuf	13	U.S. Provisional Patent Application No.: 60/053,240, Titled: TCP/IP Network Accelerator and Method of Use, Filed July 18, 1997, Inventors: Jolitz et al., Assignee: InterProphet LLC.

Examiner	Date Considered 1/3/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.